

**Notice of Allowability**

Application No.

10/663,813

Applicant(s)

NAKAMURA, JUNICHI

Examiner

Jeff Piziali

Art Unit

2629

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to the Amendment filed 10 July 2006.
2. ☒ The allowed claim(s) is/are 1, 4-12, 14, and 15 (renumbered as claims 1-12).
3. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) ☒ All    b) ☐ Some\*    c) ☐ None    of the:
    1. ☒ Certified copies of the priority documents have been received.
    2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
    3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
  5. ☐ CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
    - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached
      - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
    - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

**Attachment(s)**

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date \_\_\_\_\_
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413), Paper No./Mail Date \_\_\_\_\_
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other \_\_\_\_\_

  
Jeff Piziali

19 September 2006

## DETAILED ACTION

### *Priority*

1. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

### *Allowable Subject Matter*

2. Claims 1, 4-12, 14, and 15 (renumbered as claims 1-12) are allowed.

3. The following is an examiner's statement of reasons for allowance:

The present invention comprises an optoelectronic-device driver. The closest prior art, Sato et al (US 5,712,652 A) discloses an optoelectronic-device substrate [Fig. 4; 301], comprising: a common electrode [Fig. 1; 12] applied with a fixed potential (see Fig. 5; Column 9; Lines 62-64); a pixel electrode [Fig. 1; 3]; a storage unit [Fig. 1; 100] for storing pixel data [Fig. 1; video signals transmitted through data lines 1-na and 1-nb, displaying black on a normally white display] (see Column 24, Lines 46-56); a phase-inversion circuit [Fig. 1; 15] that outputs a phase-inversion signal [Fig. 1; AC drive signal through line 8-n] for inverting a phase of pixel data from the storage unit (see Column 9, Lines 28-61); a first switch [Fig. 1; 9] for generating a data-inversion signal [Fig. 1; output of first transfer gate 9], based on the phase-inversion signal, the data inversion signal being a signal that inverts signal phase of display data between a positive potential [Fig. 5; +Vo] and a negative potential [Fig. 5; -Vo]; and a second switch [Fig. 1; 11] for switching between the data-inversion signal from the first switch and a

Art Unit: 2629

zero-data signal [Fig. 1; reset signal through line 10] that indicates that a potential equivalent to the fixed potential applied to the common electrode should be applied to the pixel electrode (see Column 12, Lines 1-20), the second switch selecting the data-inversion signal when pixel data is stored in the storage unit [Fig. 1; transfer gate 9 is selectively turned on by the presence of black pixel data], and the zero-data signal when the pixel data is not stored in the storage unit [Fig. 1; transfer gate 11 is selectively turned on by the absence of black pixel data], the selected one of the data-inversion signal and the zero-data signal being transmitted to the pixel electrode (see Column 24, Lines 46-56).

However, Sato does not expressly teach the first switch generates a data-inversion signal by switching between output of a ground voltage and a different voltage output from the storage unit in accordance with the phase-inversion signal, as instantly claimed. This distinct structural and operational arrangement has been incorporated into both pending independent claims (i.e. claims 1 and 12), thereby rendering them allowable.

Any comments considered necessary by the applicants must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

*Conclusion*

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jeff Piziali whose telephone number is (571) 272-7678. The examiner can normally be reached on Monday - Friday (6:30AM - 3PM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bipin Shalwala can be reached on (571) 272-7681. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.



Jeff Piziali  
19 September 2006



BIPIN SHALWALA  
SUPERVISORY PATENT EXAMINER  
TECHNOLOGY CENTER 2600